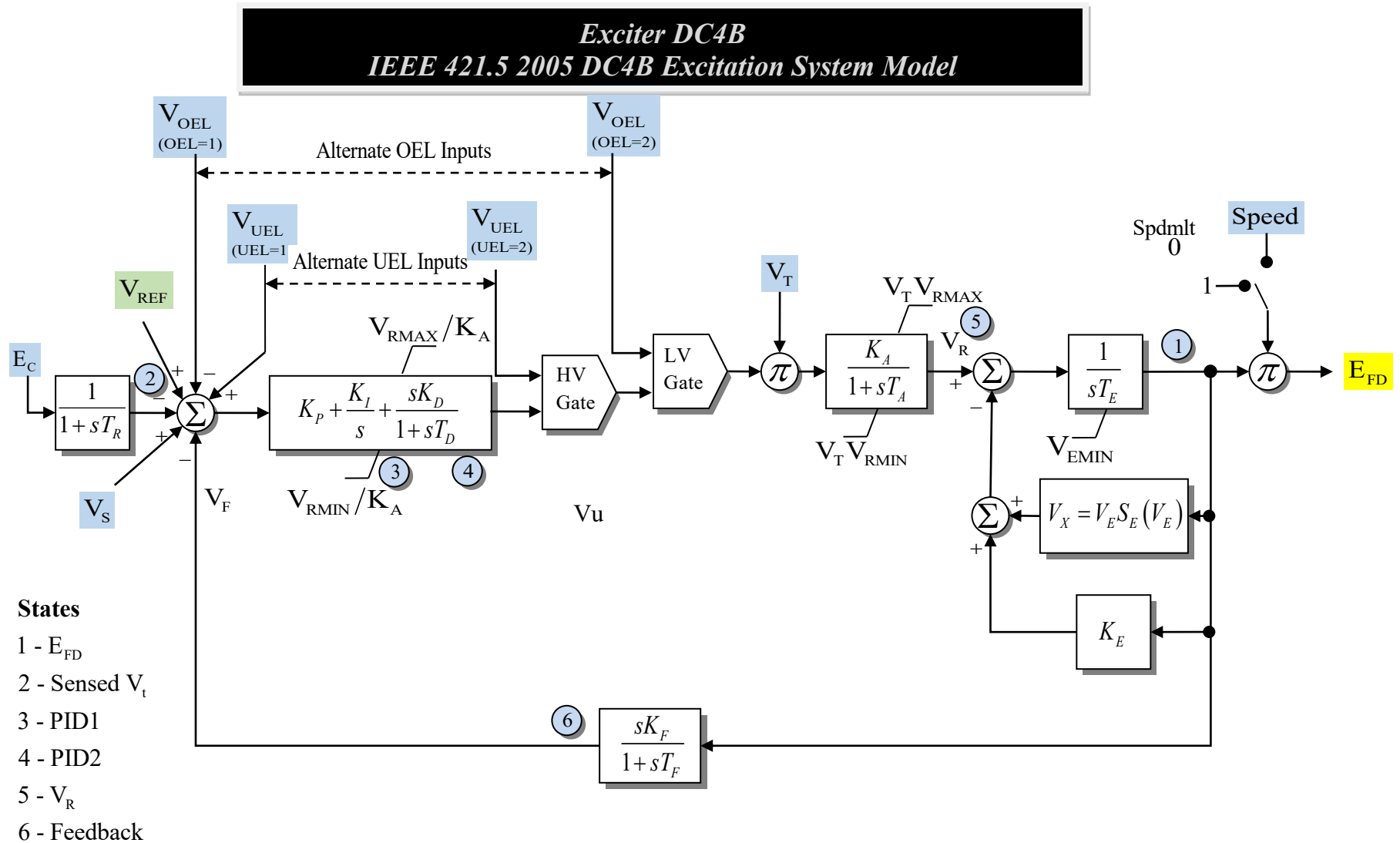


Exciter DC4B and ESDC4B



DC4B model supported by PSSE
ESD4B model supported by PSLF

Parameters for DC4B:

OEL	OEL input: if = 2, LV gate; if < 2, subtract from error signal
UEL	UEL input: if = 2, HV gate; if < 2, add to error signal
Tr	Filter time constant, sec.
Kp	Proportional gain
Ki	Integral gain
Kd	Derivative gain
Td	Derivative time constant, sec.
Vrmax	Maximum controller output, p.u.
Vrmin	Minimum controller output, p.u.
Ka	Gain, p.u.
Ta	Time constant, sec. (> 0.)
Ke	Exciter field resistance line slope margin, p.u.
Te	Exciter time constant, sec. (> 0.)
Kf	Rate feedback gain
Tf	Rate feedback time constant, sec.
Vemin	Exciter minimum output
E1	Field voltage value, 1
SE1	Saturation factor at E1
E2	Field voltage value, 2
SE2	Saturation factor at E2

Parameters for ESDC4B:

Tr	Filter time constant, sec.
Ka	Gain, p.u.
Ta	Time constant, sec. (> 0.)
Kp	Proportional gain
Ki	Integral gain
Kd	Derivative gain
Td	Derivative time constant, sec.
Vrmax	Maximum controller output, p.u.
Vrmin	Minimum controller output, p.u.
Ke	Exciter field resistance line slope margin, p.u.
Te	Exciter time constant, sec. (> 0.)
Kf	Rate feedback gain
Tf	Rate feedback time constant, sec.
E1	Field voltage value, 1
SE1	Saturation factor at E1
E2	Field voltage value, 2
SE2	Saturation factor at E2

Vemin	Exciter minimum output
OEL	OEL input: if = 2, LV gate; if < 2, subtract from error signal
UEL	UEL input: if = 2, HV gate; if < 2, add to error signal
Spdmlt	If = 1, multiply output (Efd) by generator speed